



#WeAreIn

Join the thinkers, builders, and problem-solvers behind tomorrow's technology.

As a **Senior Engineer Functional Verification (f/m/div)** in our Research & Development team, you'll have the opportunity to merge creativity with your technical expertise by shaping the future of technology, driving groundbreaking projects, and bringing new ideas to life.

You new responsibilities include

- Designing and verifying **SoC architectures**, including **subsystem** and **IP integration**
- Developing **test plans**, **testbenches** and **verification environments** for system-level IP and peripheral verification
- Creating and executing **C-based tests**, **APIs**, and **VIPs** to achieve full system and code coverage
- Performing **power-aware verification** of UPF intent and power modes
- **Automating and optimizing verification flows** to improve efficiency and quality
- Contributing to an **AI-driven, agentic verification framework**

You are best equipped for those tasks if you have

- A Master's degree in **Electrical Engineering**, **Computer Science**, or a related field
- **1-3 years of experience** with metric-driven verification and SystemVerilog, C, and Python
- Knowledge of **ARM-based microcontrollers** (e.g. Cortex-M, AHB, AXI) is a plus
- Strong **analytical, communication, and collaboration skills**
- **Fluent English**; German is a plus

Are you in?



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